## RO4730G3 $3^{\text {TM }}$ Antenna Grade Laminates Fabrication Guidelines

## DRILLING CONDITIONS:

Surface speeds greater than 500 SFM and chip loads less than $0.002^{\prime \prime}(0.05 \mathrm{~mm})$ should be avoided whenever possible. When using small diameter drills ( $<0.0135^{\prime \prime}$ ) use below a $0.002^{\prime \prime}$ chip load to avoid random breakage of drill bits.

## Recommended Ranges:

| Surface Speed: | $300-500$ SFM (90 to $150 \mathrm{~m} / \mathrm{min})$ |
| :--- | :--- |
| Chip Load: | $0.002^{\prime \prime}-0.004^{\prime \prime} / \mathrm{rev} .(0.05-0.10 \mathrm{~mm})$ |
| Retract Rate: | $500-1000$ IPM 500 IPM $(12.7 \mathrm{~m} / \mathrm{min})$ for tools less than $0.0135^{\prime \prime}$ |
|  | $(0.343 \mathrm{~mm}), 1000$ IPM $(25.4 \mathrm{~m} / \mathrm{min})$ for all others. |
| Tool Type: | Standard carbide |
| Tool Life: | $2000-3000$ hits |

Hole quality should be used to determine the effective tool life rather than tool wear. These materials will yield good hole quality when drilled with bits which are considered worn by epoxy/glass standards. Unlike epoxy/glass, RO4730G3 ${ }^{\text {TM }}$ material hole roughness does not increase significantly with tool wear. Typical values range from $8-25 \mathrm{~mm}$ regardless of hit count (evaluated up to 8000 hits). The roughness is directly related to the ceramic filler size and provides topography that is beneficial for hole-wall adhesion. Drilling conditions used for epoxy/glass boards have been found to yield good hole quality with hit counts in excess of 2000.

A minimum PTH spacing of 12 mil wall to wall for electrically isolated vias and 6-8 mils for thermal and shielding vias that redundantly connect copper layers (to prevent sphere edge to edge drilling and chemical ingress) and a hole wall roughness min spec of 2 mils.

## CALCULATING SPINDLE SPEED AND INFEED:



## QUICK REFERENCE TABLE:

| Tool Diameter | Spindle Speed (kRPM) | Infeed Rate (IPM) |
| :---: | :---: | :---: |
| $0.0100^{\prime \prime}(0.254 \mathrm{~mm})$ | 95.5 | 190 |
| $0.0135^{\prime \prime}(0.343 \mathrm{~mm})$ | 70.7 | 141 |
| $0.0160^{\prime \prime}(0.406 \mathrm{~mm})$ | 95.5 | 190 |
| $0.0197^{\prime \prime}(0.500 \mathrm{~mm})$ | 77.6 | 190 |
| $0.0256^{\prime \prime}(0.650 \mathrm{~mm})$ | 60.0 | 180 |
| $0.0258^{\prime \prime}(0.655 \mathrm{~mm})$ | 60.0 | 180 |
| $0.0295^{\prime \prime}(0.749 \mathrm{~mm})$ | 51.8 | 155 |
| $0.0354^{\prime \prime}(0.899 \mathrm{~mm})$ | 43.2 | 130 |
| $0.0394^{\prime \prime}(1.001 \mathrm{~mm})$ | 38.8 | 116 |
| $0.0453^{\prime \prime}(1.151 \mathrm{~mm})$ | 33.7 | 101 |
| $0.0492^{\prime \prime}(1.257 \mathrm{~mm})$ | 31.1 | 93 |
| $0.0531^{\prime \prime}(1.349 \mathrm{~mm})$ | 28.8 | 86 |
| $0.0625^{\prime \prime}(1.588 \mathrm{~mm})$ | 24.5 | 74 |
| $0.0925^{\prime \prime}(2.350 \mathrm{~mm})^{\wedge}$ | 16.5 | 50 |
| $0.1250^{\prime \prime}(3.175 \mathrm{~mm})$ | 15.0 | 45 |

*Conditions stated are tapered from 200SFM and 0.002" chip load up to 400 SFM and 0.003 "chip load.

## DEBURRING:

RO4730G3 materials can be deburred using conventional nylon brush scrubbers.

## DESMEAR:

RO4730G3 materials may be desmeared using traditional chemical or plasma processes. Recommended plasma conditions are provided below:

## Plasma Desmear Cycle:

| Frequency: | 40 KHz |
| :--- | :--- |
| Voltage: | $500-600 \mathrm{~V}$ |
| Power: | $4000-5000 \mathrm{Watts}$ |
| Pre-Heat to $\mathbf{6 0}{ }^{\circ} \mathbf{C}$ using: |  |
| Gases: | $90 \% \mathrm{O} 2,10 \% \mathrm{~N} 2$ |
| Pressure: | 250 mTORR |
| Desmear using: | $75 \% \mathrm{O} 2,15 \% \mathrm{CF} 4,10 \% \mathrm{~N} 2$ |
| Gases: | 250 mTORR |
| Pressure: | $10-30$ minutes |
| Time: |  |

## COPPER PLATING:

No special treatments are required prior to electroless copper plating. Board should be processed using conventional epoxy/glass procedures. Desmear of drilled holes is not typically required, as the high $\operatorname{Tg}\left(280^{\circ} \mathrm{C}\left[536^{\circ} \mathrm{F}\right]\right)$ resin system is not prone to smearing during drill. Resin can be removed using a standard CF4/O2 plasma cycle or a double pass through an alkaline permanganate process should smear results from aggressive drilling practices.


Figure 1: Cross-section of a typical RO4730G3 laminate plated through-hole. Note that the appearances of the special filler and slightly increased holewall roughness are normal for this product. Additionally, the LoPro foil surface contains a thin coating of modifed base resin, which appears colorless. We suggest discussing hole quality/appearance with a Rogers' Technical Service Engineer prior to processing RO4730G3 laminates for the first time.

## IMAGING/ETCHING:

Panel surfaces may be mechanically and/or chemically prepared for photoresist. Standard aqueous or semiaqueous photoresists are recommended. Any of the commercially available copper etchants can be used.

## SOLDERMASK:

Any screenable or photoimageable solder masks typically used on epoxy/glass laminates bond very well to the surface of RO4730G3 material. Mechanical scrubbing of the exposed dielectric surface prior to solder mask application should be avoided as an "as etched" surface provides for optimum bonding.

## HASL and REFLOW:

RO4730G3 material baking requirements are comparable to epoxy/glass. In general, facilities which do not bake epoxy/glass boards will not need to bake RO4730G3 boards. For facilities that do bake epoxy/glass as part of their normal process, we recommend a $1-2$ hour bake at $250^{\circ} \mathrm{F}-300^{\circ} \mathrm{F}\left(121^{\circ} \mathrm{C}-149^{\circ} \mathrm{C}\right)$.

Prolonged exposure in an oxidative environment may cause changes to the dielectric properties of hydrocarbon based materials. The rate of change increases at higher temperatures and is highly dependent on the circuit design. Although Rogers' high frequency materials have been used successfully in innumerable applications and reports of oxidation resulting in performance problems are extremely rare, Rogers recommends that the customer evaluate each material and design combination to determine fitness for use over the entire life of the end product.

## SHELF LIFE:

Rogers' high frequency copper clad laminates, not etched material, can be stored indefinitely under ambient room temperatures $\left(55-85^{\circ} \mathrm{F}, 13-30^{\circ} \mathrm{C}\right.$ ) and humidity levels. At room temperature, the dielectric materials are inert to high humidity. However, metal claddings such as copper can be oxidized during exposure to high humidity. Standard PWB pre-exposure cleaning procedures can readily remove traces of corrosion from properly stored materials.

## ROUTING:

RO4730G3 material can be machined using carbide tools and conditions typically used for epoxy/glass. Copper foil should be etched away from the routing channels to prevent burring.

## MAXIMUM STACK HEIGHT:

The maximum stack height should be based on $70 \%$ of the actual flute length to allow for debris removal.

Example:
Flute Length: $\quad 0.300^{\prime \prime} \times 0.70=$
Backer Penetration:
Max. Stack Height:

$$
\begin{array}{r}
0.210 "(5.33 \mathrm{~mm}) \\
-0.030^{\prime \prime}(0.762 \mathrm{~mm}) \\
\hline 0.180^{\prime \prime}(4.572 \mathrm{~mm})
\end{array}
$$

## TOOL TYPE:

Carbide multifluted spiral chip breakers or diamond cut router bits.

## ROUTING CONDITIONS:

Surface speeds below 500 SFM should be used whenever possible to maximize tool life. Tool life is generally greater than 50 linear feet when routing the maximum allowable stack height.
$\begin{array}{ll}\text { Chip Load: } & \frac{0.0010-0.0015^{\prime \prime}(0.0254-0.0381 \mathrm{~mm}) / \mathrm{rev}}{300-\text { SFM }} \\ \text { Surface Speed: }\end{array}$

QUICK REFERENCE TABLE:

| Tool Diameter | Spindle Speed | Lateral Feed Rate |
| :---: | :---: | :---: |
| $1 / 32$ | 40 k RPM | 50 IPM |
| $1 / 16$ | 25 k RPM | 31 IPM |
| $3 / 32$ | 20 k RPM | 25 IPM |
| $1 / 8$ | 15 k RPM | 19 IPM |

MULTILAYER BOARD PROCESSING: Please refer to the fabrication guidelines for RO4003C ${ }^{(T \mathrm{TM})} / \mathrm{RO}^{\left(1350 B^{(T M)} / \mathrm{RO}^{(18335}\right.}{ }^{(\mathrm{TM)}}$ Materials titled "RO4003C/RO4350B/RO4835 Laminate Circuit Processing Guidelines" PUB \#92-433.

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